

FIG.1

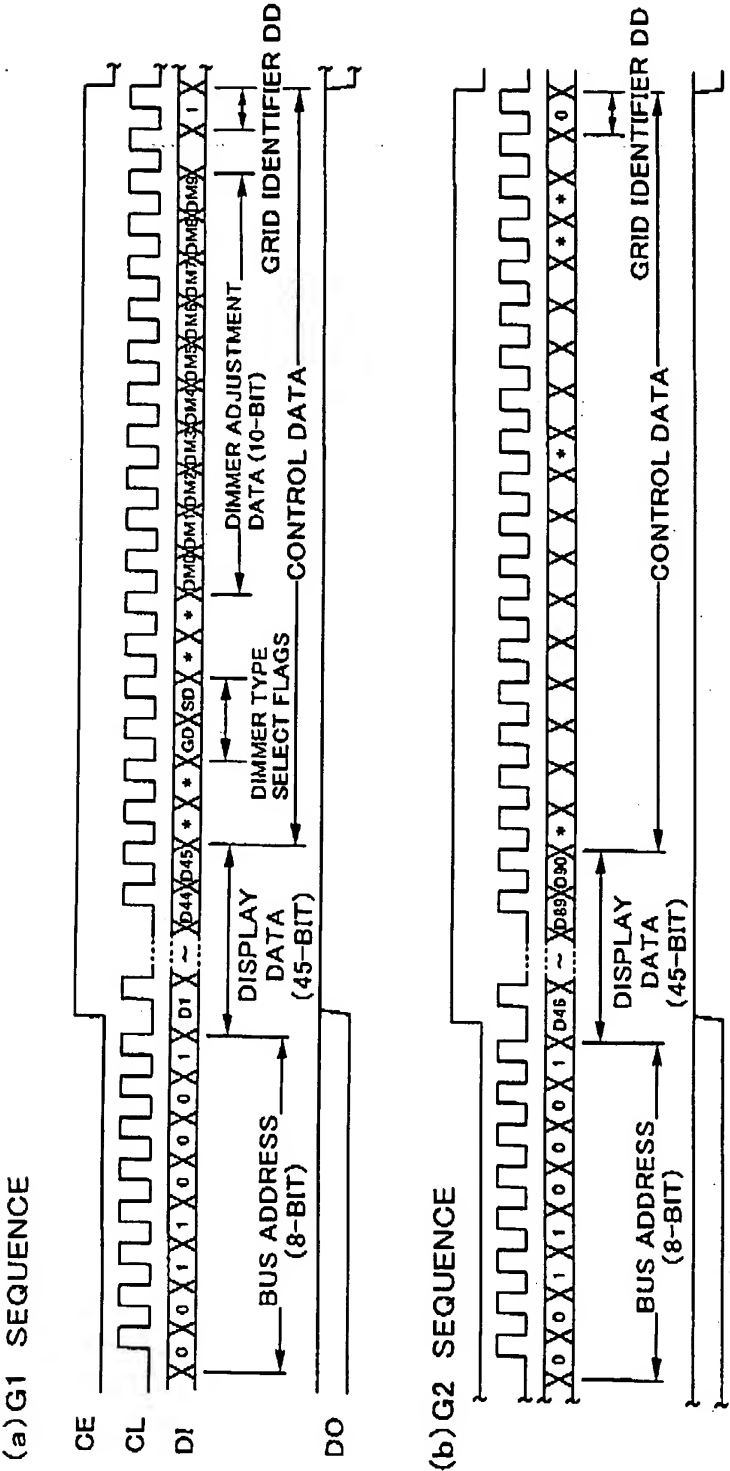
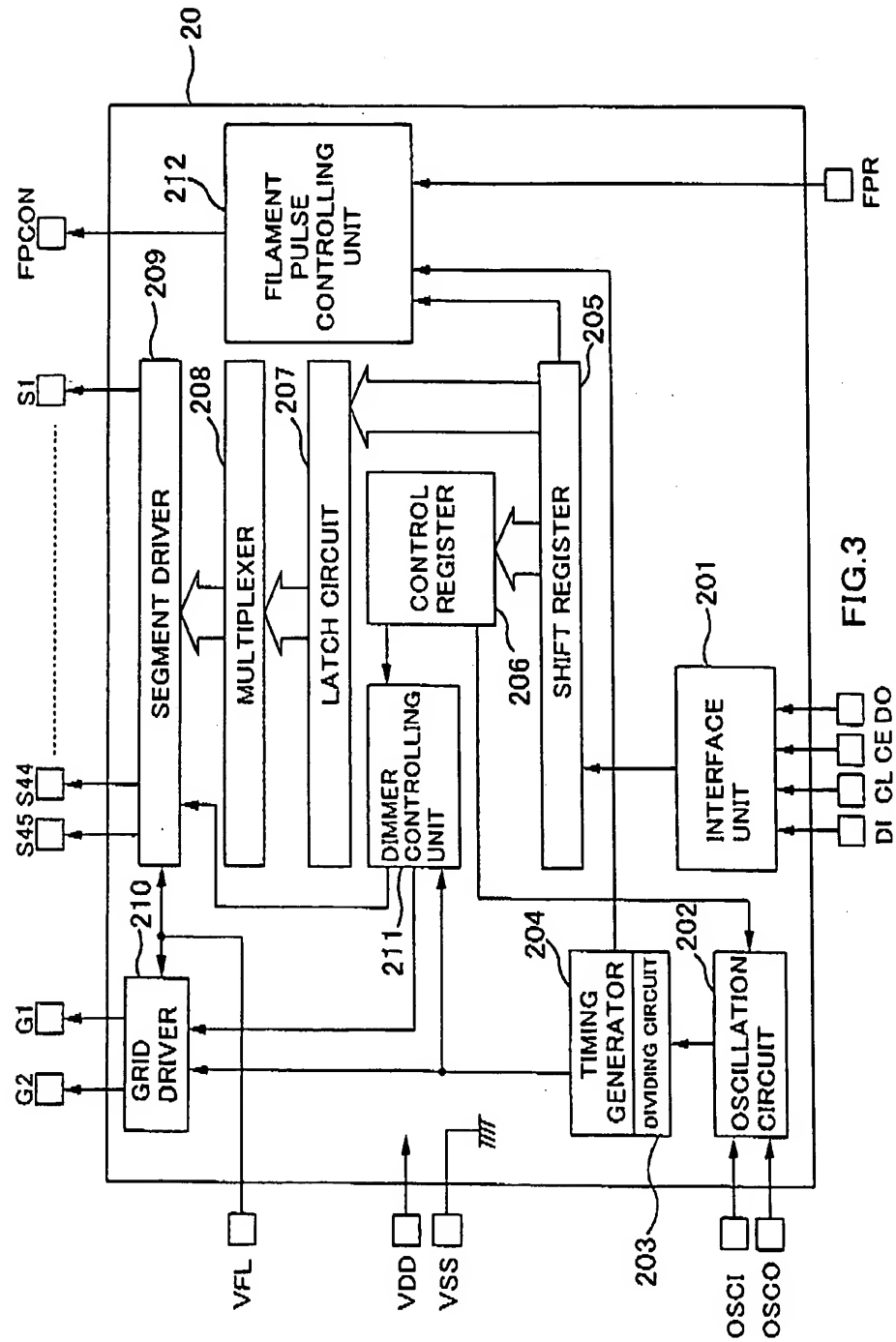


FIG.2



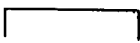



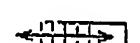

DIMMER TYPE SELECT FLAGS		OUTPUT STATUS	OUTPUT WAVEFORM	
GD	SD	G <sub>n</sub> AND S <sub>n</sub> TERMINALS	G <sub>n</sub> TERMINAL	S <sub>n</sub> TERMINAL
0	0	SETTING NOT ALLOWED	—	—
0	1	DIMMER ADJUSTMENT FOR S <sub>n</sub> OUTPUT ONLY		
1	0	DIMMER ADJUSTMENT FOR G <sub>n</sub> OUTPUT ONLY		
1	1	DIMMER ADJUSTMENT FOR BOTH G <sub>n</sub> AND S <sub>n</sub> OUTPUTS		

FIG.4

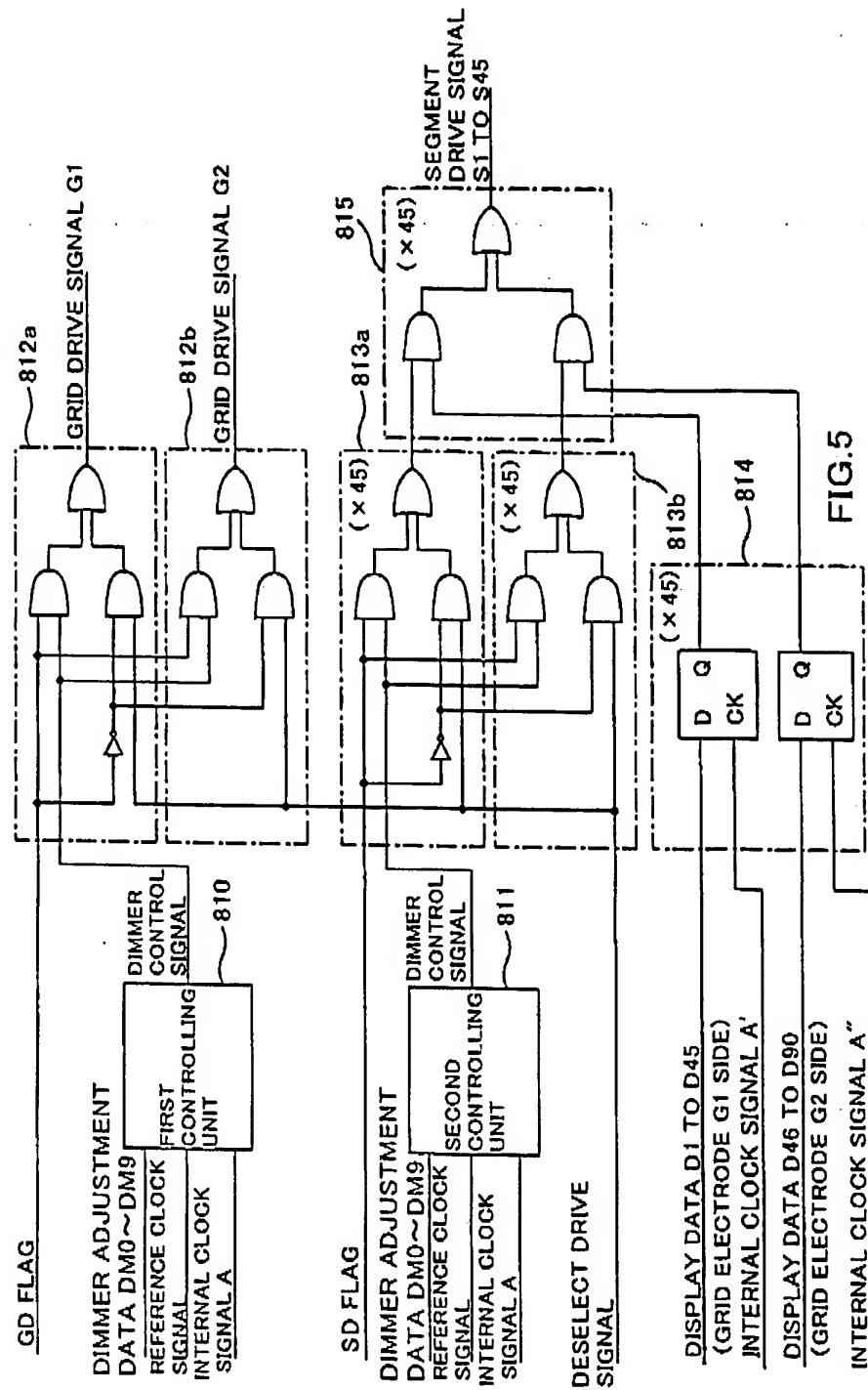


FIG.5

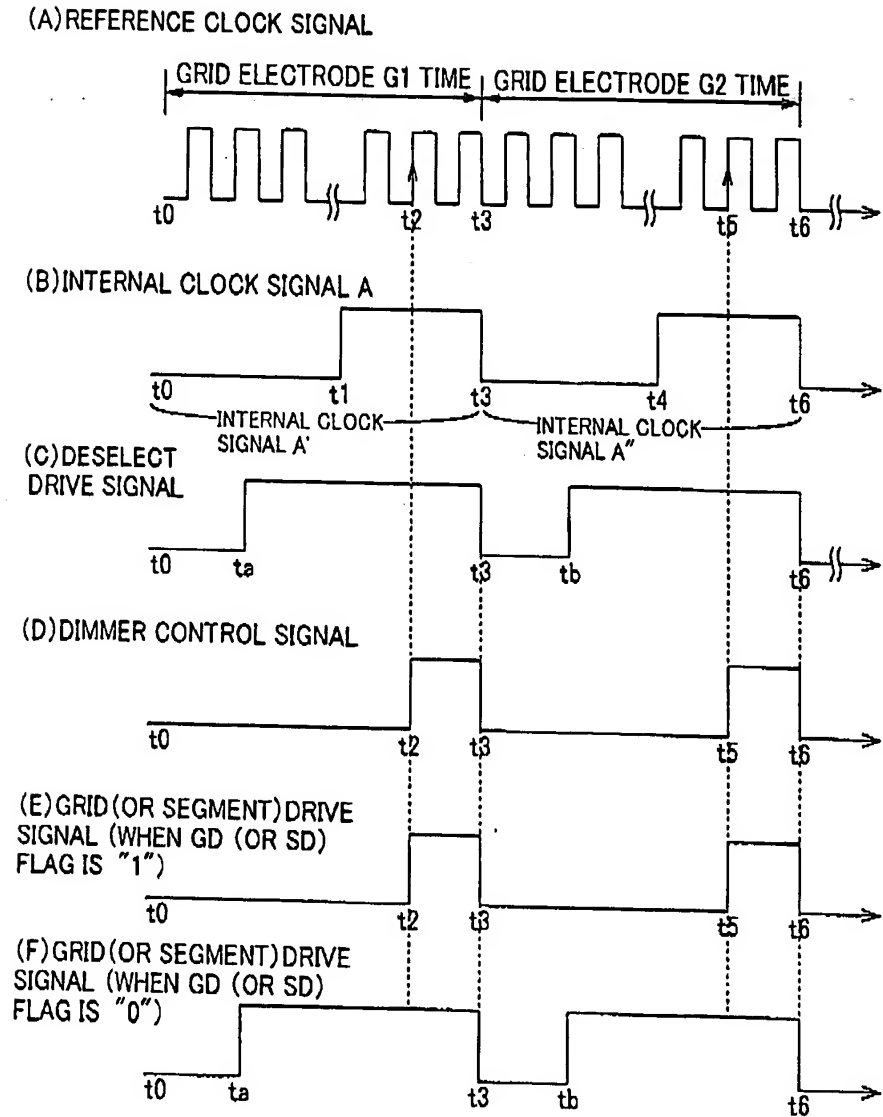


FIG.6

(a)

DIMMER ADJUSTMENT DATA											DIMMER VALUE (TW/T)
DM9	DM8	DM7	DM6	DM5	DM4	DM3	DM2	DM1	DM0		
0	0	0	0	0	0	0	0	0	0	0/1024	
0	0	0	0	0	0	0	0	0	1	1/1024	
0	0	0	0	0	0	0	0	1	0	2/1024	
										}	
1	1	1	1	1	1	1	1	0	0	1020/1024	
1	1	1	1	1	1	1	1	0	1	1021/1024	
1	1	1	1	1	1	1	1	1	0	1022/1024	
1	1	1	1	1	1	1	1	1	1	SETTING NOT ALLOWED	

FIG.7A

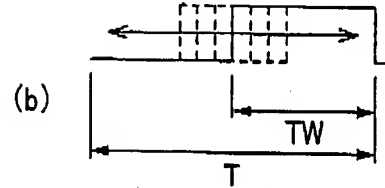
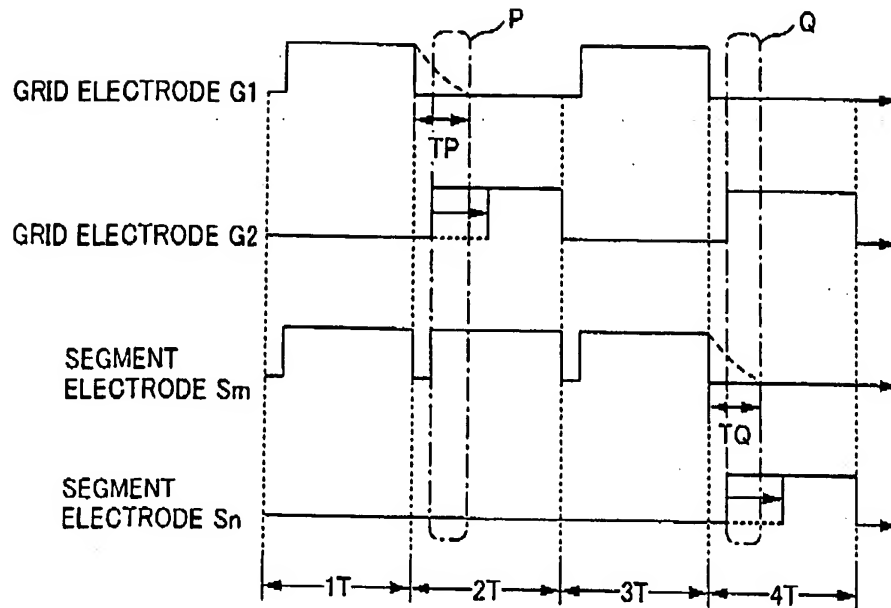
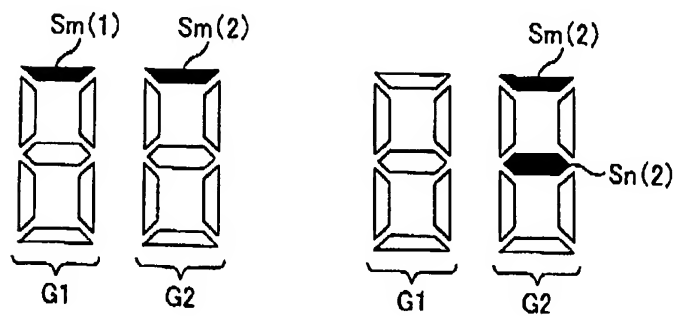


FIG.7B



(a)

FIG.8A



(b)

(c)

FIG.8B

FIG.8C